

Abstracts

A GaAs Integrated Circuit for Wideband Digital Quadrature Demodulation

R.J. Inkol, V. Szwarc, L. Desormeaux, M. Esonu and D. Al-Khalili. "A GaAs Integrated Circuit for Wideband Digital Quadrature Demodulation." 1995 MTT-S International Microwave Symposium Digest 95.3 (1995 Vol. III [MWSYM]): 1011-1014.

An integrated circuit has been designed to perform signal processing functions including digital quadrature demodulation on a sampled intermediate frequency signal in electronic warfare receivers. A -3 dB bandwidth exceeding 80 MHz is achieved using a fully pipelined, parallel architecture implemented on a GaAs gate array.

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